

Fig. 4. A double-loop network in triple-node failure in Case II) where $n = 2p$.

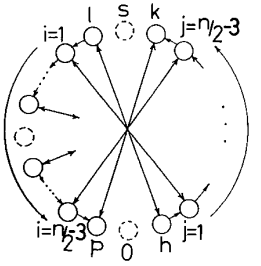


Fig. 5. A double-loop network in triple-node failure in Case III) where $n = 2p$ and the distance between a faulty node pair is s .

nodes 0 and $p + 1$ are assumed to be faulty. Then we have

$$E_1 = \sum_{i=1}^{p-2} (n - 2(i+1) + 2i) + \sum_{j=1}^{p-3} (n - 2(j+1) + 2j) + 2(n-3) \\ = n^2 - 5n + 4.$$

Let E_2 denote the number of communicable node pairs in a double-node failure with $D_f = s - 1$ or $n - s + 1$. Then we get $E_2 = n^2/2 - 2n + 1$ from Table I. Let E denote the total number of communicable node pairs for all triple-node failures where two of three are fixed at nodes 0 and $p + 1$. Then we have

$$E = (n-3)E_2 - E_1 \\ = (n^3 - 9n^2 + 24n - 14)/2.$$

There exist two types of networks in Case III): there exists some pair with $D_f = s$ or none. We first investigate the case of $D_f = s$. Fig. 5 shows an example of these networks where faulty nodes are 0, s , and i (or j). In order to make i (or j) such that the distance between any two faulty nodes is not $s + 1$ or $n - s - 1$, we set i (or j) $\neq h, k, l, p$. Therefore, the number of triple-node failures that two faulty nodes are fixed at nodes 0 and s is $n - 6$. When only two nodes 0 and s are assumed to be faulty, the number of communicable node pairs (i, \cdot) or (j, \cdot) is, respectively, $n - (2i + 3)$ or $n - (2j + 3)$. And then the number of communicable node pairs (\cdot, i) or (\cdot, j) is, respectively, $2i + 1$ or $2j + 1$. Let F_1 denote the total number of communicable node pairs for all i and j . Then we have

$$F_1 = 2 \sum_{i=1}^{p-3} (n - (2i + 3) + (2i + 1)) \\ = n^2 - 8n + 12.$$

We can see that F_2 (the number of communicable node pairs in a double-node failure with $D_f = s$) is $n^2/2 - 2n + 2$ from Table I. Let F denote the total number of communicable node pairs in a triple-

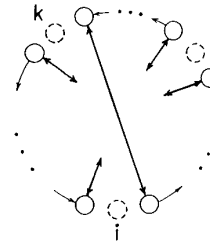


Fig. 6. A double-loop network in triple-node failure where the distance between any faulty node pair is not s .

node failure when two faulty nodes are fixed at nodes 0 and s . Then we get the following expression:

$$F = (\text{the number of communicable node pairs when only 0 and } s \text{ are faulty}) \\ - (\text{the number of communicable node pairs whose counterpart is node } i \text{ or } j \text{ where } i \text{ or } j \text{ is the third faulty node candidate}) \\ = (n - 6)F_2 - F_1 \\ = n^3/2 - 6n^2 + 22n - 24.$$

The network shown in Fig. 6 is such that the distance between any faulty node pair is not s . In this case, we can easily obtain the number of communicable node pairs in a double-node failure, i.e., $2n - 3C_2$.

We can calculate the numbers of communicable node pairs in other different cases in the similar manner as mentioned above.

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A Fast and Flexible Thinning Algorithm

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Abstract—A fast serial and parallel algorithm for thinning digital patterns is presented. The processing speed is faster than the algorithms in the literature [1]–[3] in that it reads pixels along the edge of the input pattern rather than all pixels in each iteration. Using this algorithm, an experiment is conducted and the patterns such as "X," "H," "A," "moving body," and "leaf" are tested. The results show that this

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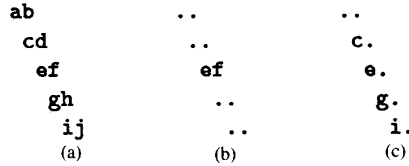


Fig. 3. A slanting pattern and its thinning.

TABLE I
THE COMPARISON OF RUN TIME (IN SECONDS) CONSUMED BY
DIFFERENT SERIAL THINNING ALGORITHMS

Algorithm	Pattern			
	"H"	"A"	"body"	"leaf"
Our Algorithm	7.80	8.57	10.60	13.68
Naccache & Shinghal	12.91	11.25	17.03	32.57

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for k := 1 to m do if h[k] = 1 then
begin
  p := FIRST[k]; x := PREV[k]; h[k] := 0;
  repeat
    z := successor(x, p); x := p;
    if 1 < B(p) < 7 and (A(p) = 1 or c(p) = 1)
      then begin deletion(Q, p); h[k] := 1 end;
    p := z;
  until loop-end-test(k);
end;
until h[1] + ... + h[m] = 0.

```

IV. PARALLEL THINNING ALGORITHM

In parallel processing, the value of a pixel at the n th iteration depends on the values of the pixel and its neighbors at the $(n - 1)$ th iteration.

In paper [3], Zhang and Suen proposed a parallel thinning algorithm ZS, which yields good results with respect to both connectivity and contour noise immunity, but algorithm ZS sometimes no longer preserves the structure of the original pattern. In Fig. 3(a) is the original pattern. Fig. 3(b) is the skeleton using algorithm ZS and Fig. 3(c) is the skeleton using our parallel thinning algorithm.

Our parallel thinning algorithm can preserve the structure of the original picture and the speed is faster than that of Algorithm ZS. We store the picture in matrix Q and $Q1$. The algorithm is shown as follows.

Function: parallel thinning

Arguments:

initial—a function that computes m , FIRST[k], PREV[k], and sets 1 to $h[k]$ ($k = 1, 2, \dots, m$).

loop-end-test (k)—a function that returns t only endpoint of k th contour loop.

successor (x, p)—a function that returns the successor point.

deletion (Q, p)—a function that deletes p in matrix Q if p satisfies the following condition.

$$1 < B(p) < 7 \text{ and } (A(p) = 1 \text{ or } c(p) = 1) \text{ and } (p[2] + p[4] * p[0] * p[6] = 0) \\ \text{or} \\ 1 < B(p) < 7 \text{ and } (A(p) = 1 \text{ or } c(p) = 1) \text{ and } (p[0] + p[6] * p[2] * p[4] = 0)$$

Algorithm:

```

initial; g := 1;
repeat
  Q := Q1; if g = 1 then g := 0 else g := 1;
  for k := 1 to m do if h[k] = 1 then

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```

begin p := FIRST[k]; x := PREV[k]; h[k] := 0;
repeat
  z := successor(x, p); x := p;
  case g of
    0: if 1 < B(p) < 7 and (A(p) = 1 or c(p) = 1)
      and (p[2] + p[4] * p[0] * p[6] = 0)
      then begin deletion(Q1, p); h[k] := 1 end;
    1: if 1 < B(p) < 7 and (A(p) = 1 or c(p) = 1)
      and (p[0] + p[6] * p[2] * p[4] = 0)
      then begin deletion(Q1, p); h[k] := 1 end;
  end case;
  p := z;
until loop-end-test(k);
end
until h[1] + ... + h[m] = 0.

```

V. THE RESULT OF EXPERIMENT

To compare the performance of our algorithms to Algorithms NS[2], ZS[3], and SV[4], all algorithms were coded in Pascal and run on an IBM personal computer XT. These algorithms were tested on digital patterns "H," "A," "body," and "leaf." The units of time taken by these algorithms are shown in Table I (serial thinning algorithms) and in Table II (parallel thinning algorithms).

VI. DISCUSSION AND CONCLUSIONS

We have just proposed the thinning algorithms which work along the contours of patterns. Comparing our algorithms to a couple of algorithms from the literature [2]–[4], we find the following.

1) They are faster, in that they do not require each pixel of the pattern to be tested except contours. As we know, "thinning" is a process that tests a dark pixel to see if it satisfies thinning conditions. If it does, delete this pixel, otherwise leave it as it is. Therefore, thinning spends most of the time on testing thinning conditions. It is faster since our proposed algorithms only need to test contours.

For example, a 556-pixel digitized character "H" shown in Fig. 4 adopted from [3]. The points marked by "." have been removed. Fig. 4(a) shows the result after the first iteration. The final result is shown in Fig. 4(b). In the first iteration, Algorithm ZS needs to test 556 pixels. In order to obtain the final result, Fig. 4(b), Algorithm ZS needs to test total 2552 pixels ($556 + 481 + 409 + 339 + 271 + 205 + 141 + 96 + 54$). However, our new algorithms only need to test 146 pixels at the first iteration and the total tested pixels is 1170 ($146 + 142 + 138 + 134 + 130 + 126 + 122 + 118 + 114$).

TABLE II
THE COMPARISON OF RUN TIME (IN SECONDS) CONSUMED BY
DIFFERENT PARALLEL THINNING ALGORITHMS

Algorithm	Pattern			
	"H"	"A"	"body"	"leaf"
Our Algorithm	9.56	10.43	14.67	22.35
Zhang & Suen	11.59	10.65	14.72	26.64
Rutovitz	16.53	14.11	21.59	42.08

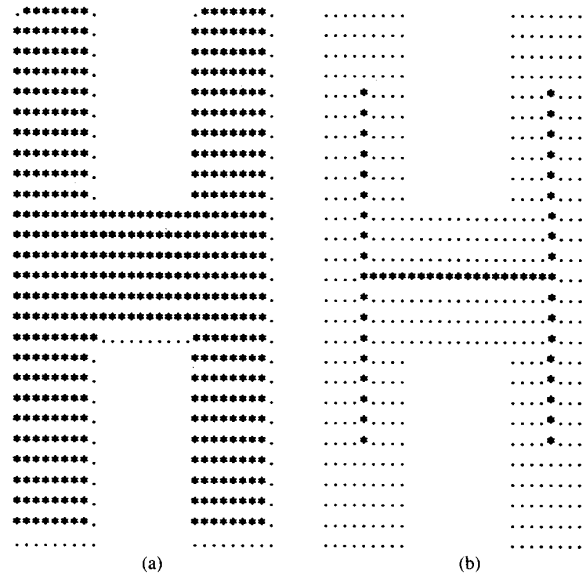


Fig. 4. Character "H" and its skeleton.

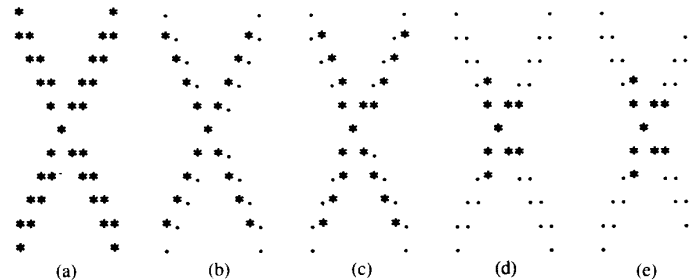


Fig. 5. Comparing different algorithms for thinning pattern "X."

2) It is structure-preserving. The simple version algorithm (SV) presented by Rutovitz [4] is a parallel algorithm. Zhang and Suen's (ZS) algorithm is also parallel. However, both of them have to overcome the disadvantage of not preserving structures. For example, using Algorithms ZS and SV to thin the digital pattern "X," the results are shown in Fig. 5(d) and (e) which lose the structures of the original pattern. (Fig. 5(a) is the original pattern, 5(b) is the result thinned by our algorithm 3 which preserves the structure). The algorithm NS can keep the shape of the original pattern as shown in Fig. 5(c), but sometimes it cannot exactly keep the basic structure as shown in Fig. 6, and it is not a parallel algorithm as we defined above. In this paper, the algorithms we presented can keep the structure of the original pattern.

For future research, it would be interesting and worthwhile to explore thinning algorithms for high-dimensional patterns. Realizing

the advantages of our approach, it would also be very interesting to polish, implement, and apply this serial and parallel thinning algorithm to a variety of pattern recognition problems including: character recognition, fingerprint recognition, signature verification, medical diagnosis, business application, industrial parts inspection, computer vision, and robotics.

However, there are still some disadvantages: 1) if a spur of two dots were added to the left end of the middle row of Appendix d, the noise will propagate. This shares the same drawback as discussed in [1]–[3], [11], and [15], and 2) even worse, the same digital patterns will totally disappear as shown in literature [1].

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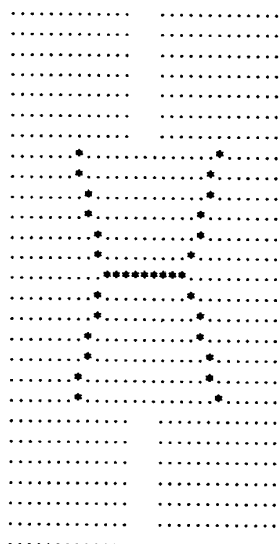
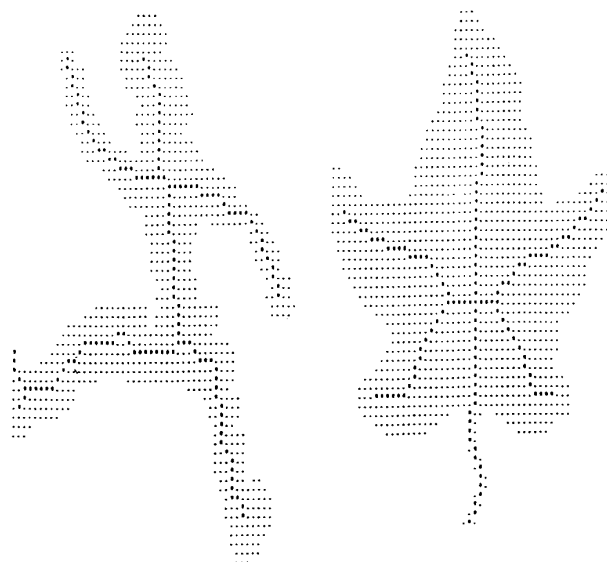


Fig. 6. Thinning of the character "H" using the algorithm NS.

APPENDIX

THINNING OF DIFFERENT DIGITAL PATTERNS USING ALGORITHM 3.



(a) A moving body

(b) A leaf

(c) The letter "A"

(d) The letter "H"

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Hamiltonian Cycles in the Shuffle-Exchange Network

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Abstract—The usefulness of the shuffle-exchange network in parallel processing applications is well established. The optimal embedding of a shuffle-exchange network of a given size depends upon the number of cycles of the shuffle permutation of that size. The cost of one method of adding fault-tolerance through reconfigurability depends upon the number of such cycles, and the manner in which they can be connected to form larger cycles.

This paper presents an exact equation for the number of cycles of a shuffle of size 2^n . We use that result to demonstrate that it is always possible to form a Hamiltonian cycle on all processors in a shuffle-exchange connected array. From this, it is apparent that there are a large number of ways of sharing spare processors among the members of many

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